

JIEJIE MICROELECTRONICS CO., LTD.

JST16A-600TW 16A TRIAC

Rev.A.1.2

DESCRIPTION:

The JST16A-R6 0 Tw 5.04 2.385 0]TJ7 Tc 0 Tw 2.385 0 1.29(6)-4 (A)-3 (-)]TJ 0.004 TTW 1.601 Tw

Average gate power dissipation ($T_j=125$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	3	kV

ELECTRICAL CHARACTERISTICS (unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33$	- -	MAX.	5	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k$	- -	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	15	mA
				20	
I_H	$I_T=500mA$		MAX.	15	mA
dV/dt	$V_D=400V$ Gate Open $T_j=125$		MIN.	100	V/s
$(dI/dt)_c$	$(dV/dt)_c=1.9$ $V_j=125$		MIN.	1.5	A/ms
t_{on}	$I_G=10mA$ $I_A=200mA$ $I_R=20mA$ $T_j=25$		TYP.	3	s
t_{off}				25	

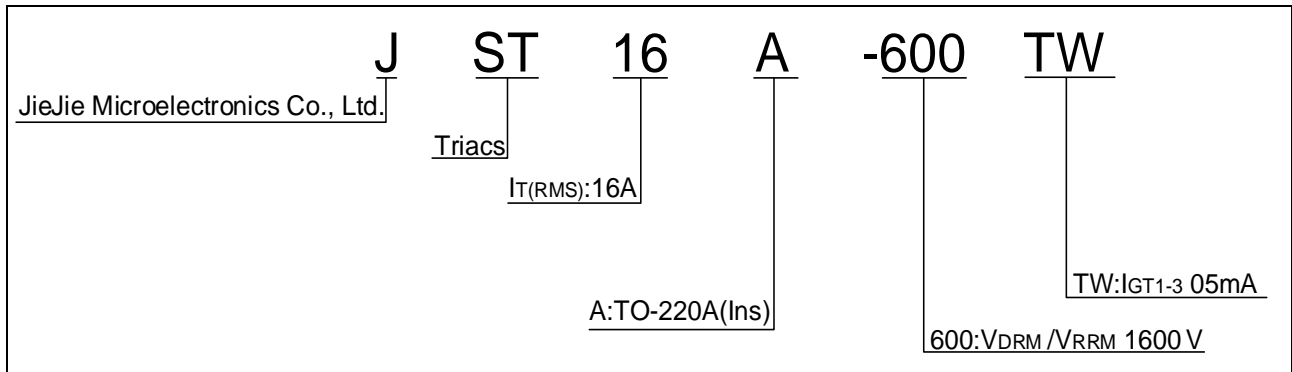
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=22.5A$ $t_p=380$ s	$T_j=25$	1.5	V
V_{TO}	Threshold voltage	$T_j=125$	0.77	V
R_D	Dynamic resistance	$T_j=125$	30	P
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25$	5	A
I_{RRM}		$T_j=125$	0.4	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-)}$			

ORDERING INFORMATION



MARKING

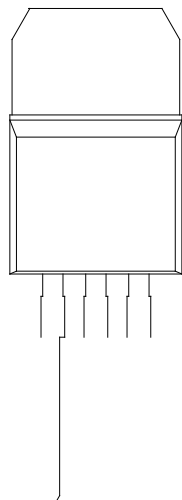
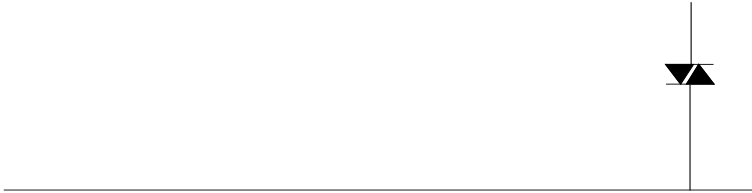


FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery	716.1 (oq)-1
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