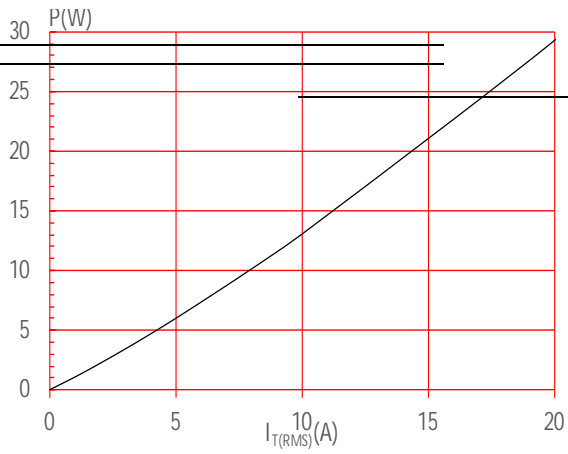








**FIG.1:** Maximum power dissipation versus RMS on-state current



**FIG.2:** RMS on-state current versus case temperature

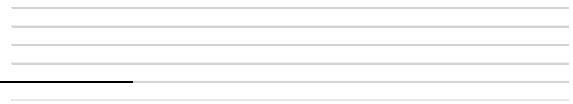
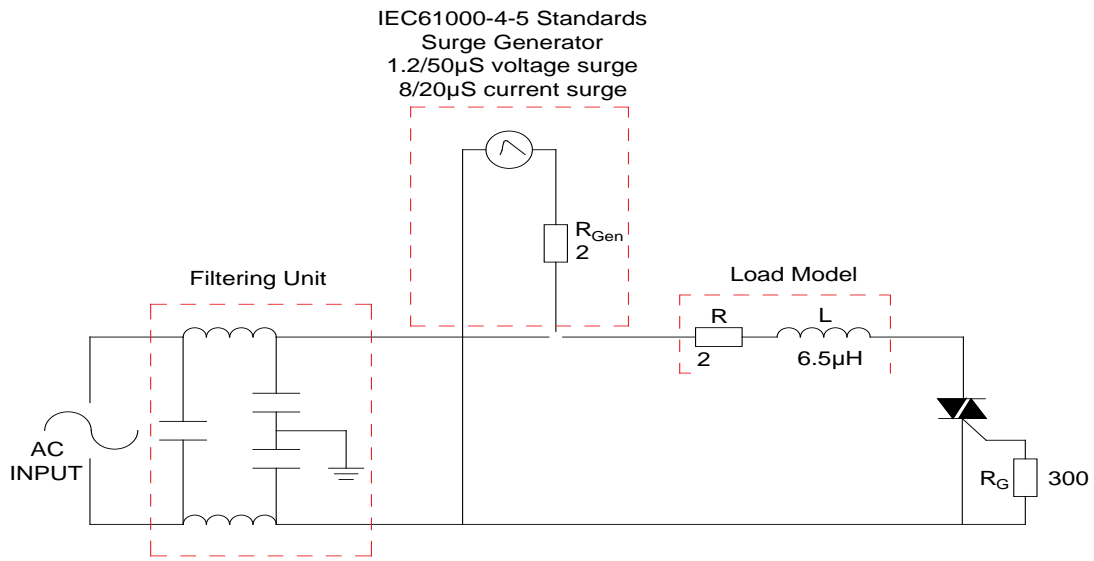


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



**ORDERING INFORMATION**

Order code	Voltage $V_{DRM}/V_{RRM}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
<b>JST16A-1200TW</b>	<b>1200</b>	<b>5</b>	<b>TO-220A(Ins)</b>	<b>50</b>	<b>Tube</b>

**Document Revision History**

Date	Revision	Changes
<u>Apr.12</u>		

PACKAGE MECHANICAL DATA

