

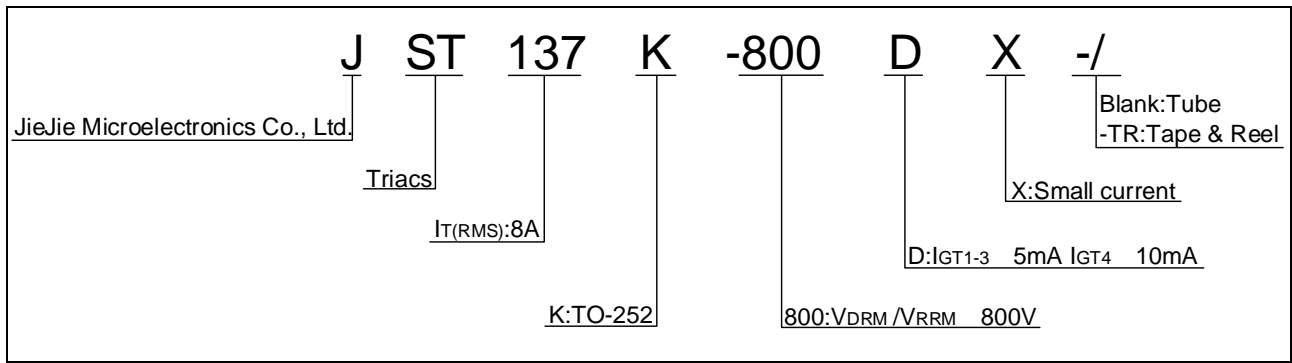
ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33$	- -	MAX.	5	mA
				10	
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	- -	MAX.	20	mA
				30	
I_H	$I_T=100mA$		MAX.	15	mA
dV/dt	$V_D=540V$ Gate Open $T_j=125$		MIN.	80	$V/\mu s$
$(dV/dt)_c$	$(dI/dt)_c=2A/ms$, $T_j=125$		MIN.	2	$V/\mu s$
t_{on}	$I_G=20mA$ $I_A=200mA$ $I_R=20mA$ $T_j=25$		TYP.	1.5	μs
t_{off}				15	

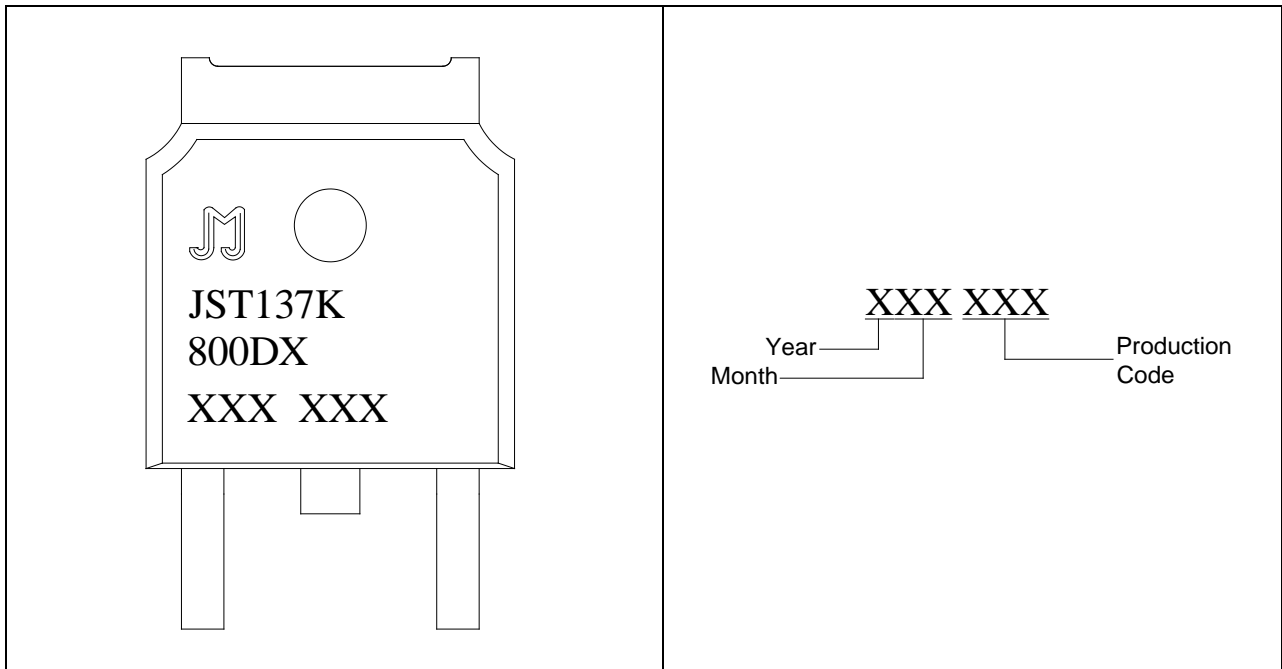
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=10A$ $t_p=380\mu s$	$T_j=25$	1.6	V
V_{TO}	Threshold voltage	$T_j=125$	0.86	V
R_D	Dynamic resistance	$T_j=125$	75	m
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25$	5	μA
I_{RRM}		$T_j=125$		

ORDERING INFORMATION



MARKING



JST137K-800DX

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

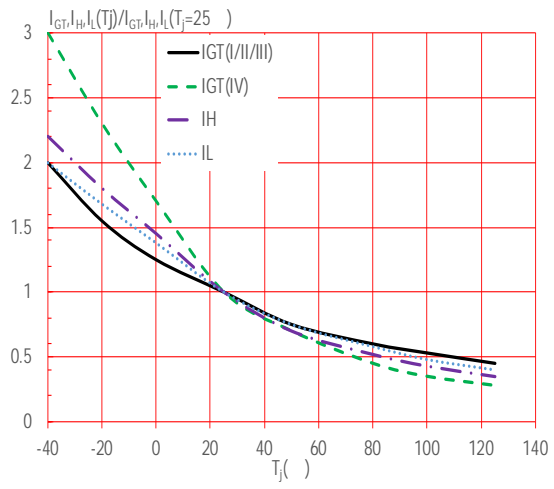


FIG.8

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST137K-800DX	800	5	10	TO-252	80	Tube
JST137K-800DX-TR					2,500	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.22, 2025	A.1	

PACKAGE MECHANICAL DATA

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.15	0		0.006
B	0.66		0.86	0.026		0.034
C	0.40		0.60	0.016		0.024
D	5.90		6.30	0.232		0.248
E	6.40		6.80	0.252		0.268
G	4.47		4.67	0.176		0.184
G1	2.18		2.38	0.086		0.094
L	1.09		1.21	0.043		0.048
L2	1.352.0.L			0.053		0.065

B2EHHB2EHHEB2H



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

 is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.
Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.