

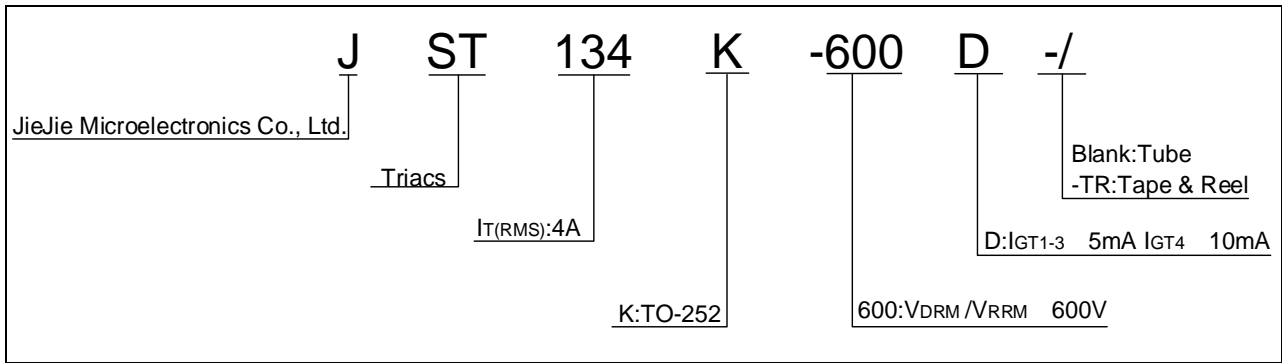
ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V R_L=33$	- -	MAX.	5	mA
				10	
V_{GT}		ALL	MAX.	1	μV

MCID448 0T /



ORDERING INFORMATION



MARKING

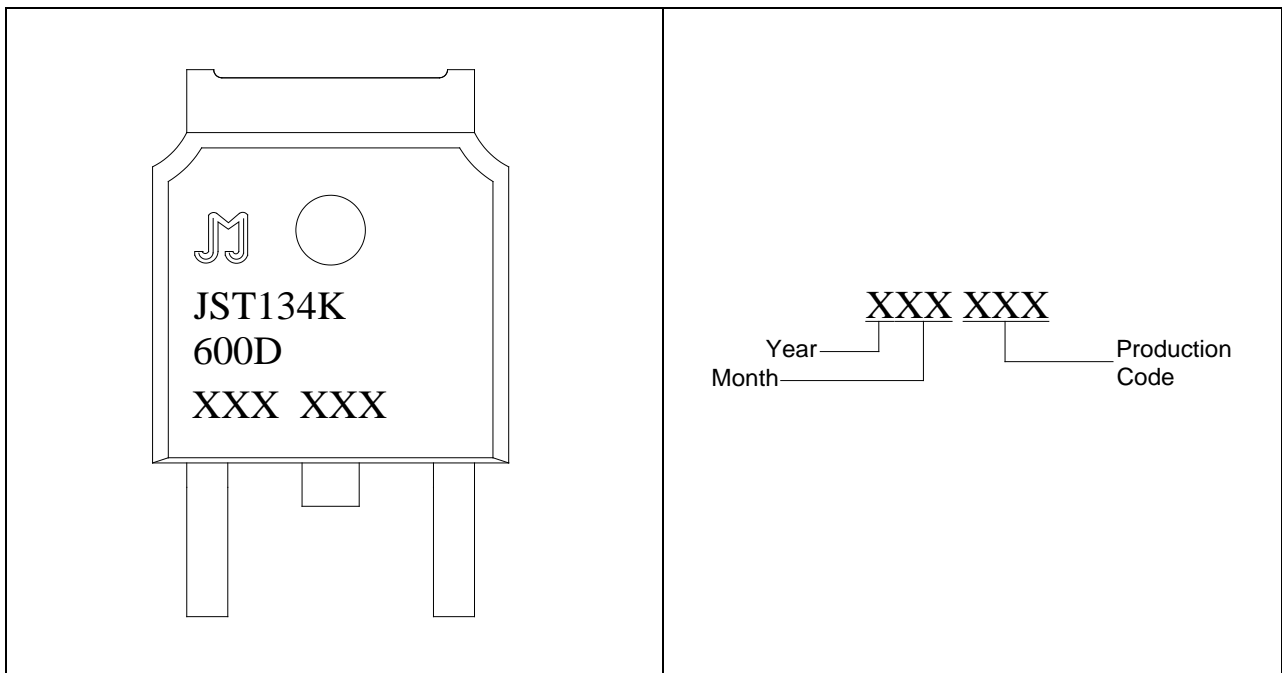


FIG.1: Maximum power dissipation versus RMS on-state current



FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

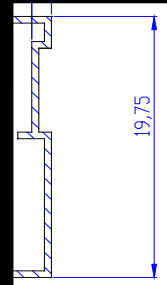


ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST134K-600D	600	5	10	TO-252	80	Tube
JST134K-600D-TR					2,500	Tape & Reel

Document Revision History

Date	Revision	Changes
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