



A7F%\$\$!, ' '\$", 5'GYbg]h]jY'G7F'

FYj'5"&"%

^ Z / W d / K E W

The MCR100-8 SCR provides high dV/dt rate with strong resistance to electromagnetic interface. It is especially recommended for use on residual current circuit breaker, straight hair, igniter etc. Complying with UL standards (File ref: E252906).Package TO-92 is RoHS compliant.

D / E & dhZ ^

Symbol	Value	Unit
I <sub>T(RMS)</sub>	0.8	A

V<sub>DRM</sub> / V<sub>R RM</sub> 800 V M

Average gate power dissipation (T <sub>j</sub> =125 )	P <sub>G(AV)</sub>	0.1	W
Peak gate power	P <sub>GM</sub>	2	W
Peak pulse voltage (T <sub>j</sub> =25 ; non-repetitive,off-state;FIG.7)	V <sub>pp</sub>	1	kV

**BCH9**%. Operating junction temperature T<sub>j</sub> is up to 125 when a resistor 1k is connected between Gate and Cathode. Without this resistor, the T<sub>j</sub> is up to 110 only.

> d Z / > , Z d Z ~ / A d n unless otherwise specified •

Gma Vc''	HYgh' 7 cbX]h]cb'	JU' i Y'			I b]h'
		A-B''	HMD''	A5L''	
I <sub>GT</sub>	V <sub>D</sub> =12V R <sub>L</sub> =33	-	50	200	μA
V <sub>GT</sub>		-	0.6	0.8	V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125	0.2	-	-	V
I <sub>L</sub>	I <sub>G</sub> =1.2 I <sub>GT</sub>	-	-	4	mA
I <sub>H</sub>	I <sub>T</sub> =0.05A	-	-	3	mA
dV/dt	V <sub>D</sub> =540V T <sub>j</sub> =125 R <sub>GK</sub> =1K	200	-	-	V/μs
	V <sub>D</sub> =540V T <sub>j</sub> =125 R <sub>GK</sub> =220	500	-	-	
t <sub>on</sub>	I <sub>G</sub> =10mA I <sub>A</sub> =20mA I <sub>R</sub> =2mA	-	2	-	μs
t <sub>off</sub>	T <sub>j</sub> =25	-	50	-	μs

^ d d / , Z d Z / ^ d / ^

Gma Vc''	DUfU a YhYf'		JU' i Yf]A5L''L'	I b]h'
V <sub>TM</sub>	I <sub>T</sub> =1A t <sub>p</sub> =380μs	T <sub>j</sub> =25	1.35	V
V <sub>TO</sub>	Threshold voltage	T <sub>j</sub> =125	0.93	V
R <sub>D</sub>	Dynamic Resistance	T <sub>j</sub> =125	0.34	
I <sub>DRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub>	T <sub>j</sub> =25	2	μA
I <sub>RRM</sub>		T <sub>j</sub> =125	0.2	mA

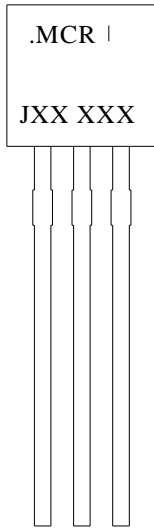
d , Z D > Z ^ / ^ d E ^

Gma Vc''	DUfU a YhYf'	JU' i Y'	I b]h'
R <sub>th(j-c)</sub>	junction to case (DC)	63	/W
R <sub>th(j-a)</sub>	junction to ambient (DC)	140	/W

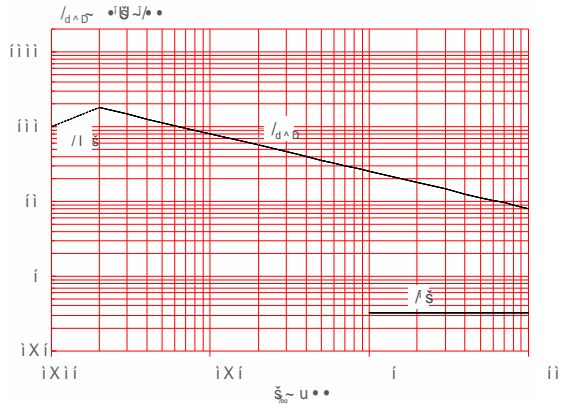
A7F%\$\$!,'

 >]Y>]Y' A]WfcY`YWhfcb]Wg'7c"z'@hX"

D Z < / E '



:= ; ") . Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10\text{ms}$ , and corresponding value of  $I^2t$  ( $di/dt < 50\text{A}/\mu\text{s}$ )



:= ; "\*" . Relative variations of gate trigger current, holding current and latching current versus junction temperature

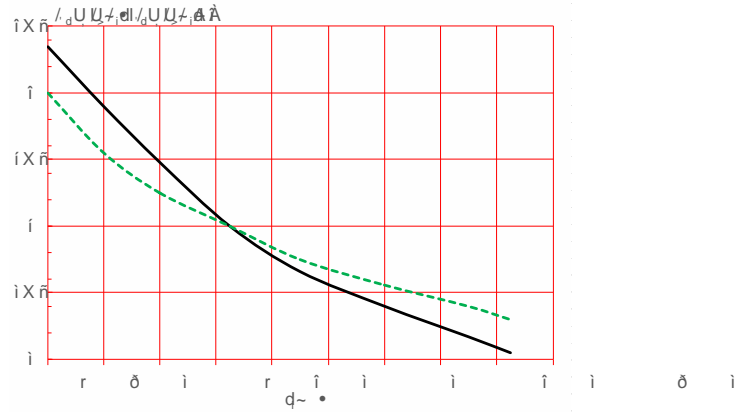
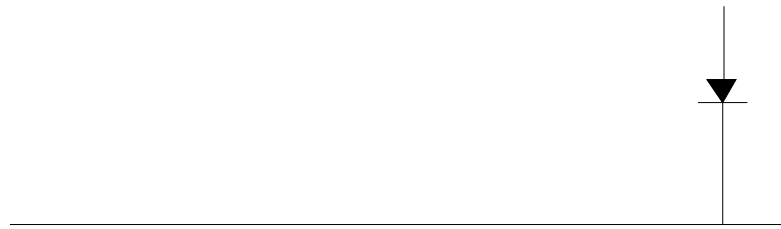


FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards.



KZ Z/E' /E&KZD d/KE

CfXYf`WcXY`	Jc`hU[Y` J8FA#JFFA`flJL`	=; Hfl 5L`	DUW_U[Y`	6UgY`ehm` fidWgL`	8Y`jYfm` a cXY`
A7F%\$\$!,'	,\$\$`	@&\$\$`	HC!-&`	%z\$\$\$`	6 i`_`DUW_`
A7F%\$\$!,!HF`				&z\$\$\$`	HUdY` /`FYY`

8cWi aYbh`FYj]g]cb`<]ghcfm`

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last update
Mar.28, 2025	A.2.0	Renew PACKAGE MECHANICAL DATA
Sept.28, 2025	A.2.1	Revise PACKAGE MECHANICAL DATA

**A7F%\$\$!,'**

>/s Zz DK

D57?5 ; 9`	C I H@=B9`	65 ;` fID7GŁ`	=BB9F`6CL` fID7GŁ`	75FHCB`6CL` fID7GŁ`
TO-92	Bulk Pack	1,000	10,000	50,000

P1	3.55	3.85	4.15
P2	5.95	6.35	6.75
, P	-1.00	0	1.00
F1 ĀF2	2.30	2.50	2.70
F1-F2	-0.10	0	0.10
W	17.50	18.00	19.00
W0	5.50	6.00	6.50
W1	8.50	9.00	9.50
W2			1.00
D0	3.80	4.00	4.20
, H	-1.00	0	1.00
L1	2.50		
H	18.00	19.00	20.00

