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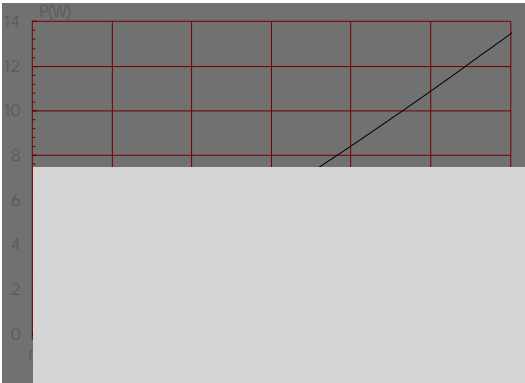


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FIG.1: Maximum power dissipation versus RMS on-state current



FIG

FIG.2: RMS on-state current versus case temperature

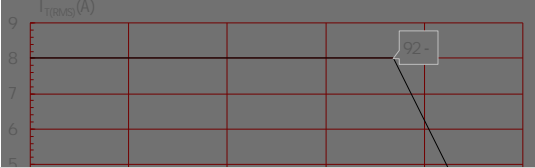


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



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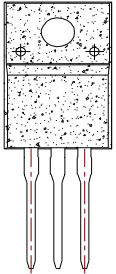
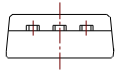


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ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(μ A)	Package	Base qty. (pcs)	Delivery mode
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PACKAGE MECHANICAL DATA



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