

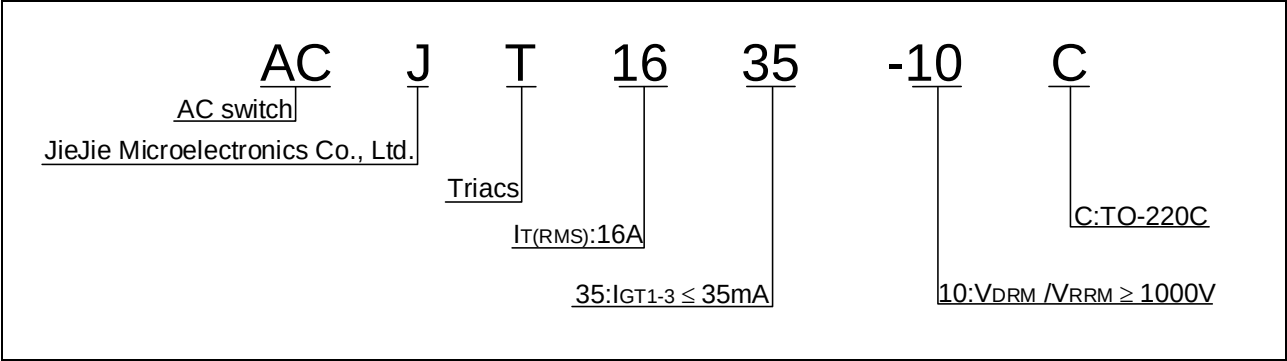


Peak pulse voltage (T _j =25 ; non-repetitive, off-state; FIG.7)	V _{pp}	4.5	kV
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ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol Test Condition Quadrant ; (t)_{24die}

ORDERING INFORMATION



MARKING

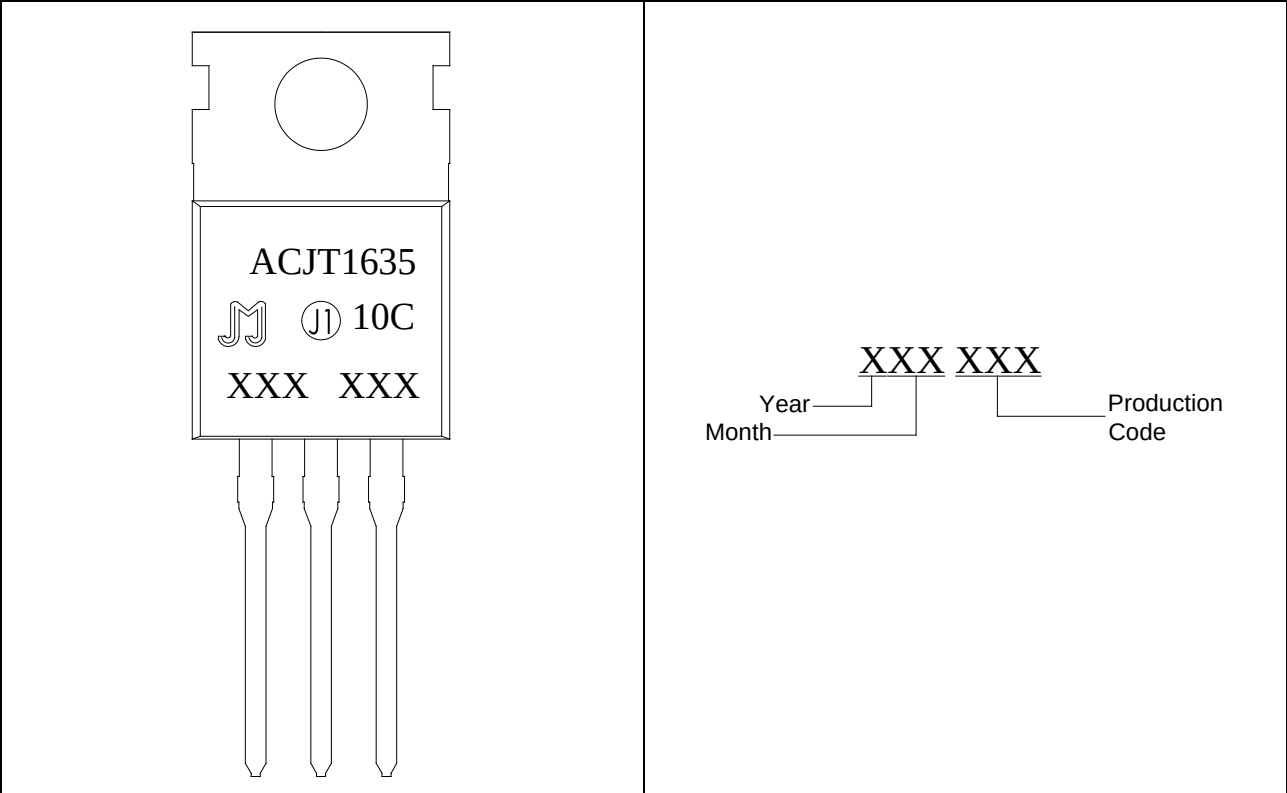


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
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PACKAGE 8-PIN DIP (S), 16-PIN DIP (S), 16-PIN DIP (S)



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